

### **Brief Bio-Data of Prof. L.M. Patnaik**

Prof. L.M. Patnaik obtained his Ph.D in 1978 in the area of Real -Time Systems, D.Sc. in 1989 in the areas of Computer Systems and Architectures, both from the Indian Institute of Science, Bangalore. During March 2008 to August 2011, he was the Vice Chancellor, Defence Institute of Advanced Technology, Deemed University, Pune. Currently he is a NASI Senior Scientist and Adjunct Professor with the National Institute of Advanced Studies, Bangalore. Prior to this he was a Professor with the Department of Computer Science and Automation and Honorary Professor with the Department of Electronic Systems Engineering at the Indian Institute of Science, Bangalore, and INSA Honorary Scientist with the National Institute of Advanced Studies, Bangalore. During the last 50 years of his long service, his teaching, research, and development interests have been in the broad areas Computer Science and Engineering and Information Technology, particularly in High Performance Computing, Mobile Computing, AI and Machine Learning. In these areas, he has over 1300 publications in refereed International Journals and refereed International Conference Proceedings including 30 technical reports, 43 co-edited books and 26 chapters in books. He has supervised over 30 Doctoral theses and over 160 Masters' theses in the above areas. He has been an Investigator for a large number of Government-sponsored research projects funded by the DST ,DAE, DoS,DRDO, DoE/MEITY and a Consultant to a few industries. One of his research papers in the area of adaptive genetic algorithms has been cited over 3900 times, a survey article on genetic algorithms over 2600 times, by Google Scholar; with an i -10 index of 162.

He has served as the President of the Advanced Computing and Communications Society, and the Computational Intelligence Society of India. He is a Distinguished Lecturer of the IEEE Region 10. He is a Distinguished Visitor of the IEEE Computer Society for the Asia-Pacific Region. As a recognition of his contributions in the areas of Electronics, Informatics, Telematics and Automation, he was awarded the Dr. Vikram Sarabhai Research Award in 1989; the Honorable Mention Award, Fourth CSI/IEEE International Symposium on VLSI Design, 1992; the Dr. Ramlal Wadhwa Award for the year 1992 by the Institution of Electronics and Telecommunication Engineers, for his contributions during the last ten years in the fields of Electronics and Telecommunication; the Platinum Jubilee Lecture Award (Computer Science), Indian Science Congress, in 1994; the Samanta Chandrasekhar Award for the year 1994 by the Orissa Science Academy; the Certificate of Appreciation "For excellent service and dedication to the objectives of the IEEE Computer Society as a Distinguished Visitor", in 1995; the VASVIK Award in the Field of Electronic Sciences and Technology, Vividhlaxi Audyogik Samshodhan Vikas Kendra,1996; the Hands for Indian IT Award for his contributions to the Indian IT (Information Technology) industry, in 1998; the "Distinguished Professional Engineer Award" by the Computer Engineering Division of the Institution of Engineers (India) in 1999; the IEEE Computer Society's "1999 Technical Achievement Award" for his contributions in the field of parallel, distributed, and soft computing, and high performance genetic algorithms; the Fourth Sir C V Raman Memorial Lecture Award in 2000; the First SVC Aiya Memorial Trust Award for Telecommunication Education, The Institution of Electronics and Telecommunication Engineers, Pune Centre, 2000; the Pandit Jawaharlal Nehru National Award for Engineering and Technology, in 1999; the Om Prakash Bhasin Award for contributions in the areas of Electronic and Information Technology for the year 2001; the FICCI Award for Innovation in Material Science, Applied Research and Space Science, 2001 -2002; the IEEE Computer Society's Meritorious Service Award, 2002; Alumni Award for Excellence in Research for Engineering, the Indian Institute of Science, 2003,

Distinguished Engineer Award of The Institution of Engineers(India), 2004; Goyal Prize for Applied Science, 2005; Honorary Fellow, the Indian Society for Technical Education, 2006; Indian Science Congress Association's Srinivasa Ramanujan Birth Centenary Award, 2007-2008; Biju Patnaik Award for Excellence in Science – 2007, in 2009; Top Management Consortium, Pune, 'Education – Defence Services Excellence Award – 2009', March 2010; Distinguished Alumnus Award, 2012, National Institute of Technology, Rourkela; Felicitation of International Neural Network Society India Chapter for outstanding contributions to Soft Computing, Dec 2014; M N Saha Memorial Award for Best Application Oriented Paper by the IETE in 2016; Life Time Achievement Award, Computer Society of India, 2017, Dr.Raja Ramanna State Award for Science Education, Government of Karnataka, 2019, Prof.S N Mitra Memorial Award of the INAE in 2020, IEEE Medal of Honour, Bangalore Section, 2022.

He is Fellow of the IEEE, The World Academy of Sciences (TWAS), Trieste, Italy, The Computer Society of India, Indian National Science Academy, Indian Academy of Sciences, National Academy of Sciences, Indian National Academy of Engineering, Maharashtra Academy of Sciences. and Honorary Fellow of Karnataka Science and Technology Academy. He is a Life Member of the VLSI Society of India, Instrument Society of India, a founder Member of the Executive Committee of the Association for the Advancement of Fault-Tolerant and Autonomous Systems; a Fellow of the Institution of Engineers. His name appears in Asia's Who's Who of Men and Women of Achievement; Directory of Distinguished Computer Professionals of India. For over one hundred and fifty International Conferences, he has served as General Chair or Program Chair or Steering Committee Chair; most of them sponsored by the Institute of Electrical and Electronics Engineers (IEEE). He has delivered over 300 Keynote/Invited Talks at several International and National Conferences/Workshops covering a broad range of topics in Computer Science and Engineering and IT, and recently on NEP. He has served on several significant committees of leading professional societies such as the IEEE, CSI, IETE and Institution of Engineers; review and policy committees of Government agencies such as the DST, DBT, MHRD, CSIR, DoS, UGC, AICTE, DRDO, MEITY and UPSC; including on the Yash Pal committee to advise on Renovation and Rejuvenation of Higher Education. His participation in the reviews of several Engg Colleges in the states of Karnataka, Kerala, Andhra Pradesh, and Maharashtra, organised by the AICTE, UGC, NBA, and TEQIP has significantly improved the quality of technical education in the country. As Vice Chancellor of the Defence Institute of Advanced Technology, Deemed University, Pune, he was responsible for conducting large number of Doctoral, Masters', short and long courses, in the field of defence technologies for service officers from the Army, Air Force, Navy; DRDO scientists, officers from Indian Ordnance Factories and Director General of Quality Assurance, and service officers from several friendly foreign countries. He had initiated three new Post - Graduate Programmes of relevance to defence services including one on Cybersecurity, a new Centre on Nanotechnology for defence applications, and a collaborative programme with the Naval Post -Graduate School, California, USA.

He has delivered several Invited lectures in USA, Canada, France, UK, Yugoslavia (erstwhile), Hungary, Switzerland, Australia, New Zealand, Hong Kong, Singapore, Malaysia, Sri Lanka and Japan.

**It needs to be emphasized that his training, as well as the research work done by him are entirely indigenously based. His contributions to Indian higher technical education system are outstanding.**

For his publications and other contributions, please visit, <http://www.lmpatnaik.in>